**SETUP 1:**

AM3352 board is a custom board as below and we have ethernet work in below setup.

**Development environment:** TI SDK 05.03.00.

**Observation:** Ping is working at Kernel.

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| **Peripheral Name** | **Peripheral Pin Name** | **AM3352** | | **Mitysom Pin** | **Remarks** |
| **BALL NUMBER** | **Congrol Register Offset** |
| Ethernet | ETH\_PHY\_RESET | D12 | 0x998 | NOT APPLICABLE | GPIO3\_16 |
| ETH\_TXCTL | J16 | 0x914 | NOT APPLICABLE |  |
| ETH\_RXCTL | J17 | 0x918 | NOT APPLICABLE |  |
| ETH\_TXCLK | K18 | 0x92c | NOT APPLICABLE |  |
| ETH\_RXCLK | L18 | 0x930 | NOT APPLICABLE |  |
| ETH\_TXD0 | K17 | 0x928 | NOT APPLICABLE |  |
| ETH\_TXD1 | K16 | 0x924 | NOT APPLICABLE |  |
| ETH\_TXD2 | K15 | 0x920 | NOT APPLICABLE |  |
| ETH\_TXD3 | J18 | 0x91c | NOT APPLICABLE |  |
| ETH\_RXD0 | M16 | 0x940 | NOT APPLICABLE |  |
| ETH\_RXD1 | L15 | 0x93c | NOT APPLICABLE |  |
| ETH\_RXD2 | L16 | 0x938 | NOT APPLICABLE |  |
| ETH\_RXD3 | L17 | 0x934 | NOT APPLICABLE |  |
| ETH\_MDIO\_CLK | M18 | 0x94C | NOT APPLICABLE |  |
| ETH\_MDIO\_DATA | M17 | 0x948 | NOT APPLICABLE |  |

AM3352

(Base Board)

Micrel Phy KSZ9021RNI

RGMII 1

Expansion I/O

EXPANSION CONNECTOR

**SETUP 2:**

AM3359 with MitySOM Processor board as below

**Development environment:** TI SDK 05.03.00 / MitySOM

**Observation:** Ping is NOT working.

We are using same Micrel Phy KSZ9021RNI in Setup 1 and 2.

MitySOM-335x

Processor

Card

with AM3359

(Base Board)

Micrel Phy KSZ9021RNI

RGMII 2

Expansion I/O

Hi,

We have used mityarm u-boot and kernel and build

**Setup the MDK**

linux-gnueabi

**U-BOOT:**

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| **Peripheral Name** | **Peripheral Pin Name** | **AM3359** | | **Mitysom Pin** | **Remarks** |
| **BALL NUMBER** | **Congrol Register Offset** |  |  |
| Ethernet | ETH\_PHY\_RESET | L18 | 0x930 | 120 | GPIO3\_10 |
| ETH\_TXCTL | R13 | 0x840 | 58 |  |
| ETH\_RXCTL | V14 | 0x844 | 60 |  |
| ETH\_TXCLK | U15 | 0x848 | 70 |  |
| ETH\_RXCLK | T15 | 0x84c | 72 |  |
| ETH\_TXD0 | V15 | 0x850 | 68 |  |
| ETH\_TXD1 | R14 | 0x854 | 66 |  |
| ETH\_TXD2 | T14 | 0x858 | 64 |  |
| ETH\_TXD3 | U14 | 0x85c | 62 |  |
| ETH\_RXD0 | V17 | 0x860 | 82 |  |
| ETH\_RXD1 | T16 | 0x864 | 80 |  |
| ETH\_RXD2 | U16 | 0x868 | 78 |  |
| ETH\_RXD3 | V16 | 0x86c | 76 |  |
| ETH\_MDIO\_CLK | M18 | 0x94C | 141 |  |
| ETH\_MDIO\_DATA | M17 | 0x948 | 143 |  |