PCN# 20260324000

Modification Kit for Revision -1 to -2

MitySBC-A5E
All Variants

Date: March 24, 2026

To: Purchasing Agents

Dear Customer,

This is an initial announcement of a change to a product that is currently offered by Critical Link. The details of this change are on the following pages.

For questions regarding this notice, contact us at info@criticallink.com

Sincerely,

Critical Link, LLC

Phone: (315) 425-4045

Fax: (315) 425-4048

PCN Number: 20260324000
PCN Date: March 24, 2026
Title: Modification Kit for Revision -1 to -2
Contact: info@criticallink.com
Phone: (315) 425-4045
Ship Date: Starting Dec 2024

Overview

Changes to MitySBC-A5E are identified in the following sections.

1 Correct USB to UART Console Reset Issue

1.1 Description of Change

The MitySBC utilizes a FT230XS-U UART to USB bridge chip for the console interface. The chip's VCCIO pin was not connected to a valid voltage level. A jumper was installed to connect the voltage to the local 1.8V supply on the board.

1.2 Reason for Change

Without this change the USB to UART console interface does not work correctly.

1.3 Anticipated Impact on Form, Fit, Function (positive / negative)

With this change, the console port operates as expected.

1.4 Anticipated Impact on Quality or Reliability (positive / negative)

With this change, the user should be able to reliably use the console port interface for the device.

2 Include a 100 Ohm Load resistor on 1.8V and 1.0V Supplies

2.1 Description of Change

For both the 1.8V and 1.0V local switched power supplies, a 100 Ohm load resistor was added to the output nodes.

2.2 Reason for Change

During startup the switching power supplies controlling the 1.8V and 1.0V rails require a 100 Ohm load on the output to ensure the state of the on-board switching MOSFETS is known. This was not included in the original design.

2.3 Anticipated Impact on Form, Fit, Function (positive / negative)

No anticipated change to form, fit, or function.

2.4 Anticipated Impact on Quality or Reliability (positive / negative)

Without the load resistors, repeated power cycling of the board can cause premature failure of the 1.8V or the 1.0V switching regulators. Adding the load resistors will increase the reliability of the board.

3 Support USB 2.0 Dual Role on USB-C connector

3.1 Description of Change

A jumper resistor that connected the ID pin on the USB3320C ULPI interface chip to +3.3V was removed. The ID pin was connected to the ID pin on the HD3SS3220IRNHR USB-C mux chip for correct operation of the ULPI PHY to support Dual Role modes of operation.

3.2 Reason for Change

Without the change, the USB 2.0 Dual Role (i.e., switching from HOST mode to PERIPHERAL mode in USB 2.0 or from Upstream to Downstream mode in USB-C terminology) was not supported.

3.3 Anticipated Impact on Form, Fit, Function (positive / negative)

The change will allow using the USB-C port in both upstream and downstream data modes.

3.4 Anticipated Impact on Quality or Reliability (positive / negative)

No change to quality or reliability is anticipated.

4 Products Affected

Details regarding the full revision history are in the MitySBC-A5E Revision History section on the Critical Link support site.

https://support.criticallink.com/redmine/projects/mitysbc_a5/wiki/Errata_and_Product_Change_Notifications

Model Number	Starting PCA	Replacement PCA
A5ED-B96-C7F-RC-SBC-X	80-001679RC-1	80-001679RC-2

Table 1: Products Affected

5 Document Revision History

Date	Version	Change Description
24-March-2026	1.0	Initial Version