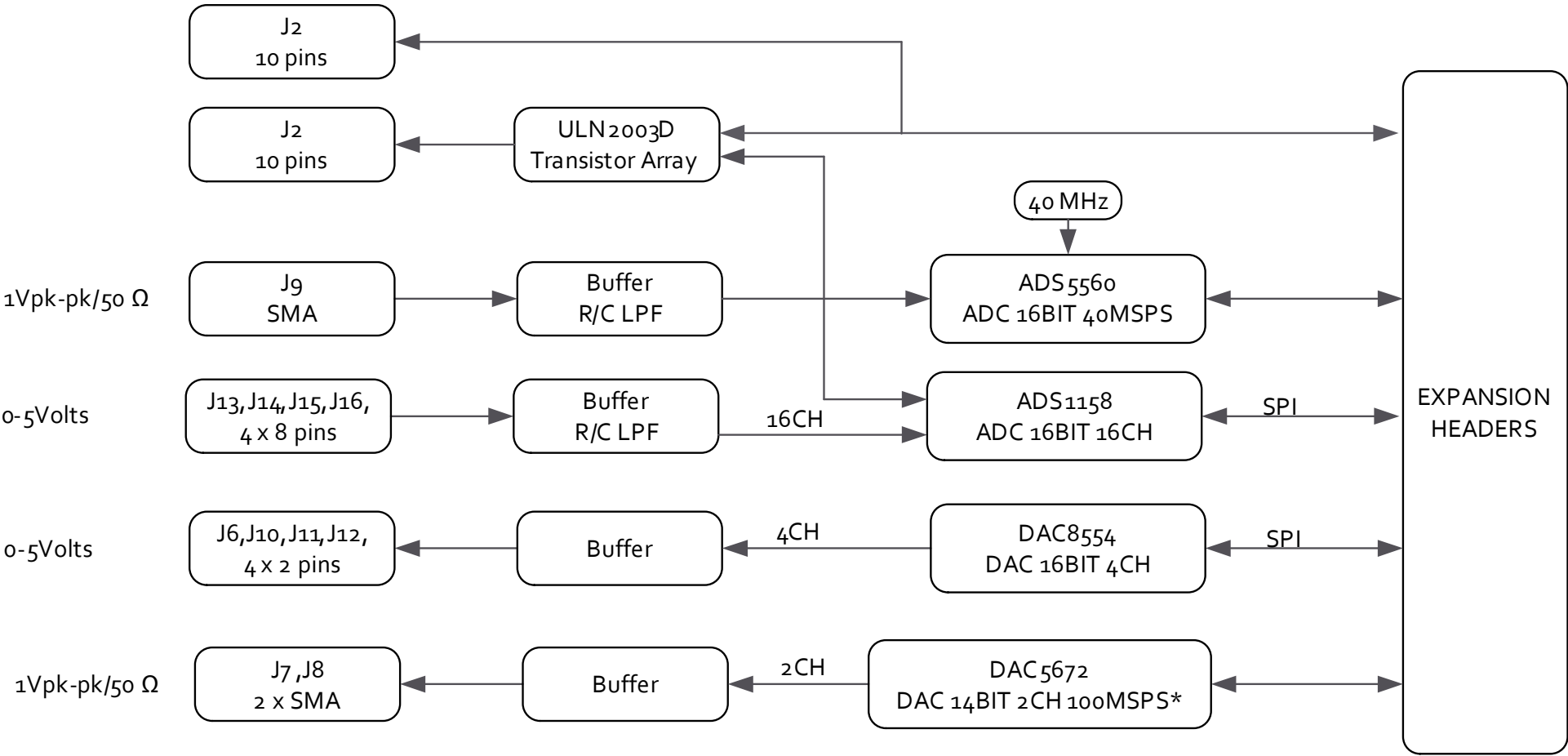


REV	DESCRIPTION	ECR No	INITIALS	DATE
-1A	Initial Release	NA	OV	5-31-12
-1B	Changes per ECR	519	AC	2-11-13



*DAC5672 Can operate at frequencies up to 200 MSPS, but will require customer to change FPGA code

PCB # 90-000173R-2_RevA
PN-PCB1
SN: # 90-000173R-2_RevA
PN-PCB-SN1
PCA # 80-000464RI-1_RevB
PN-PCA1

A

A

B

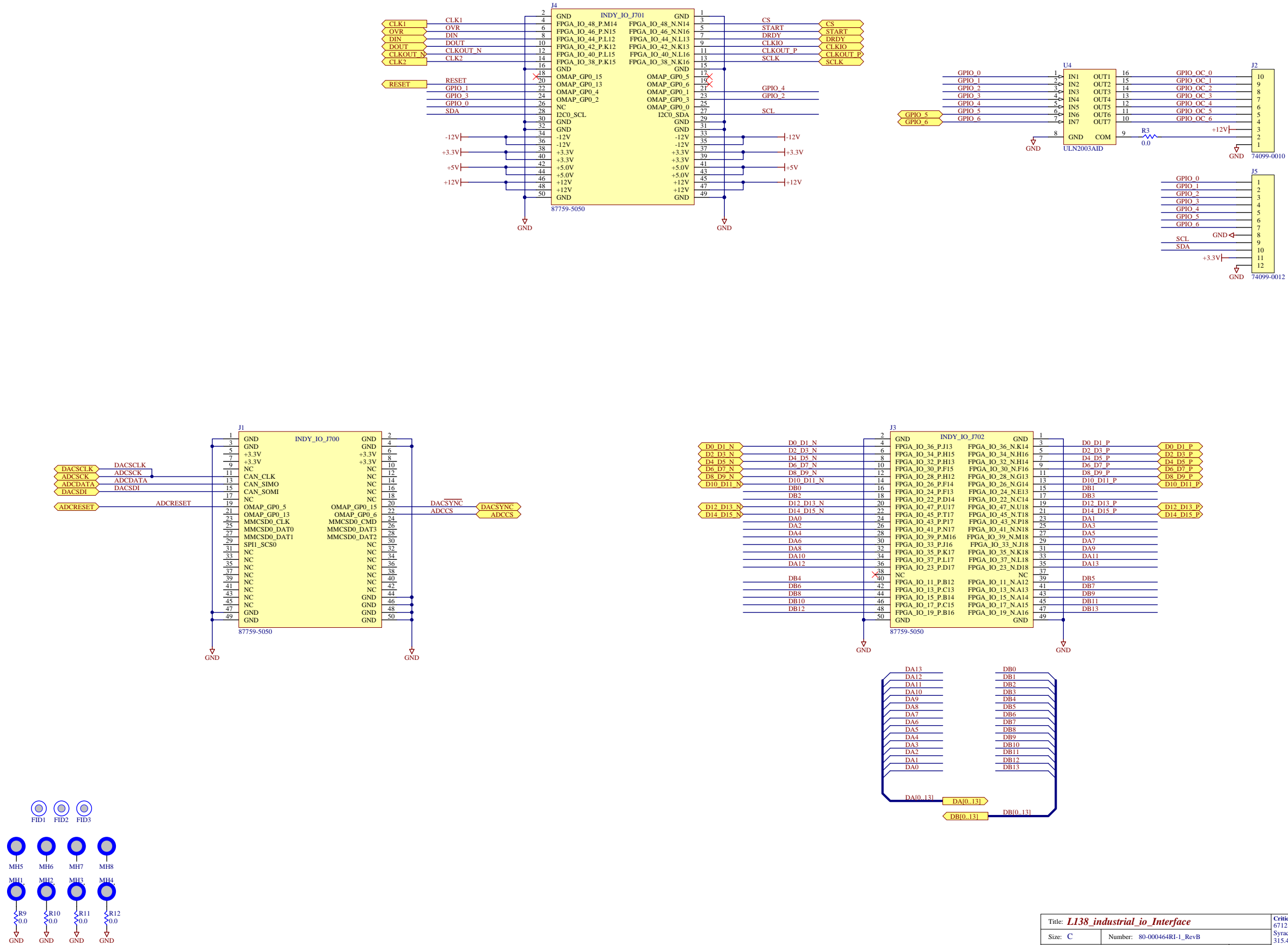
B

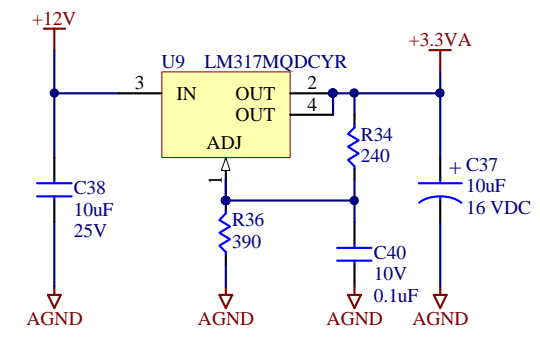
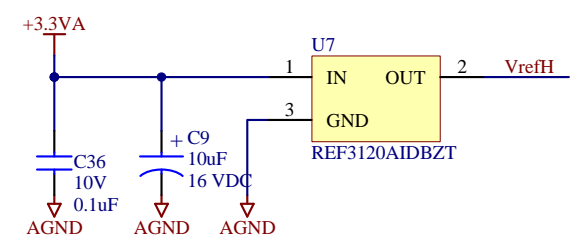
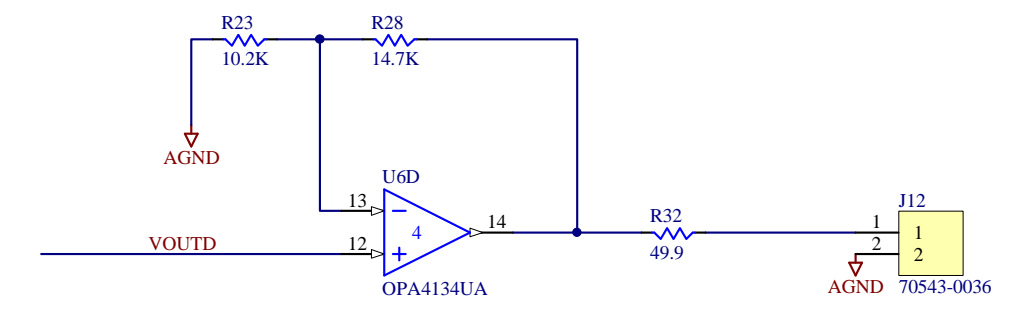
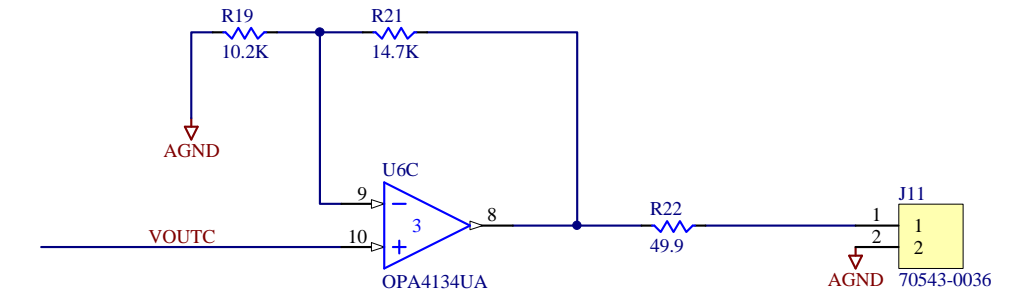
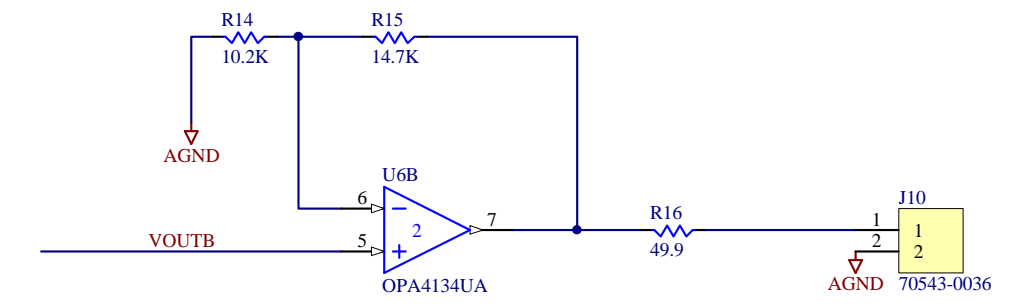
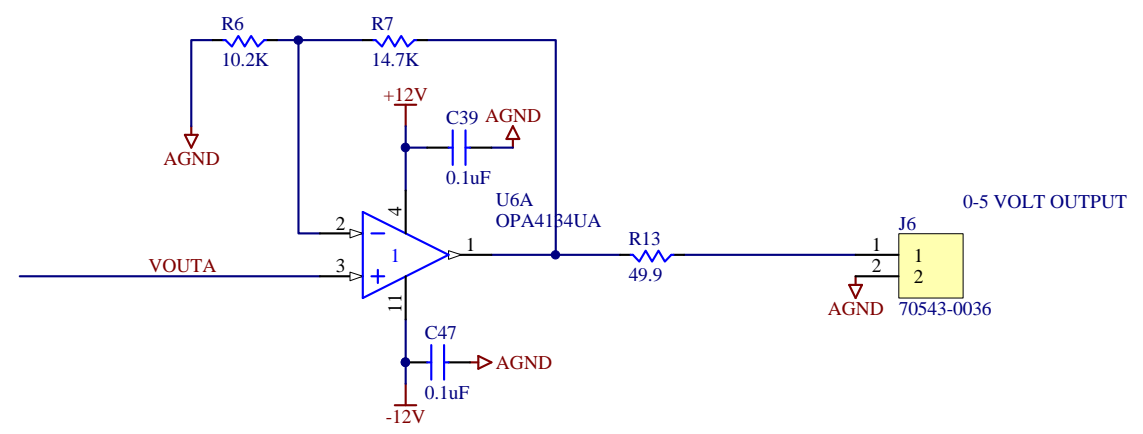
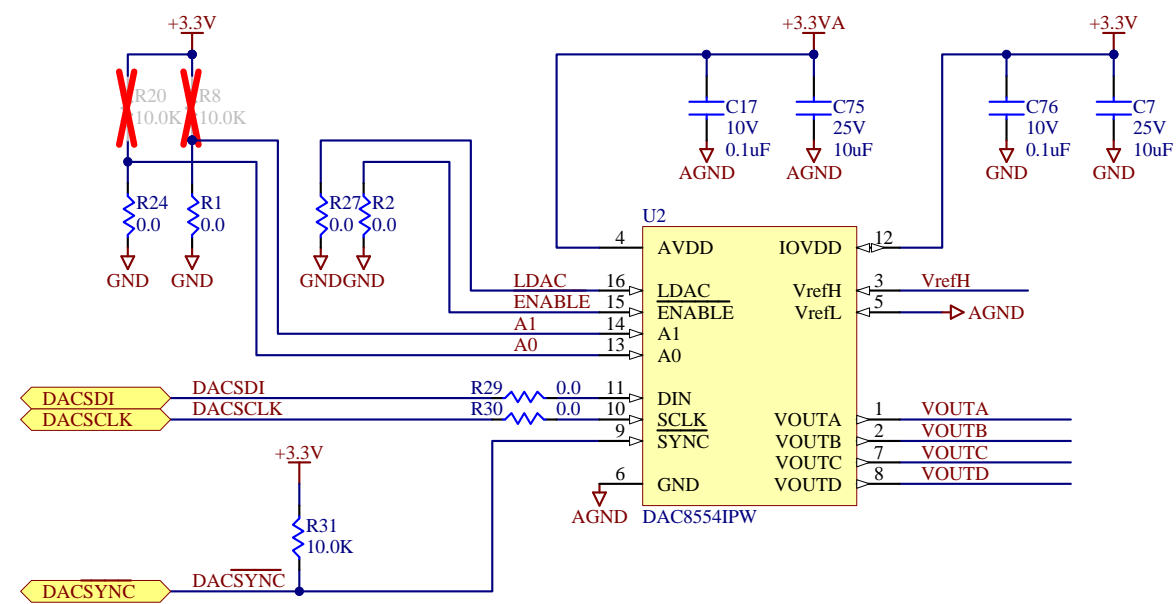
C

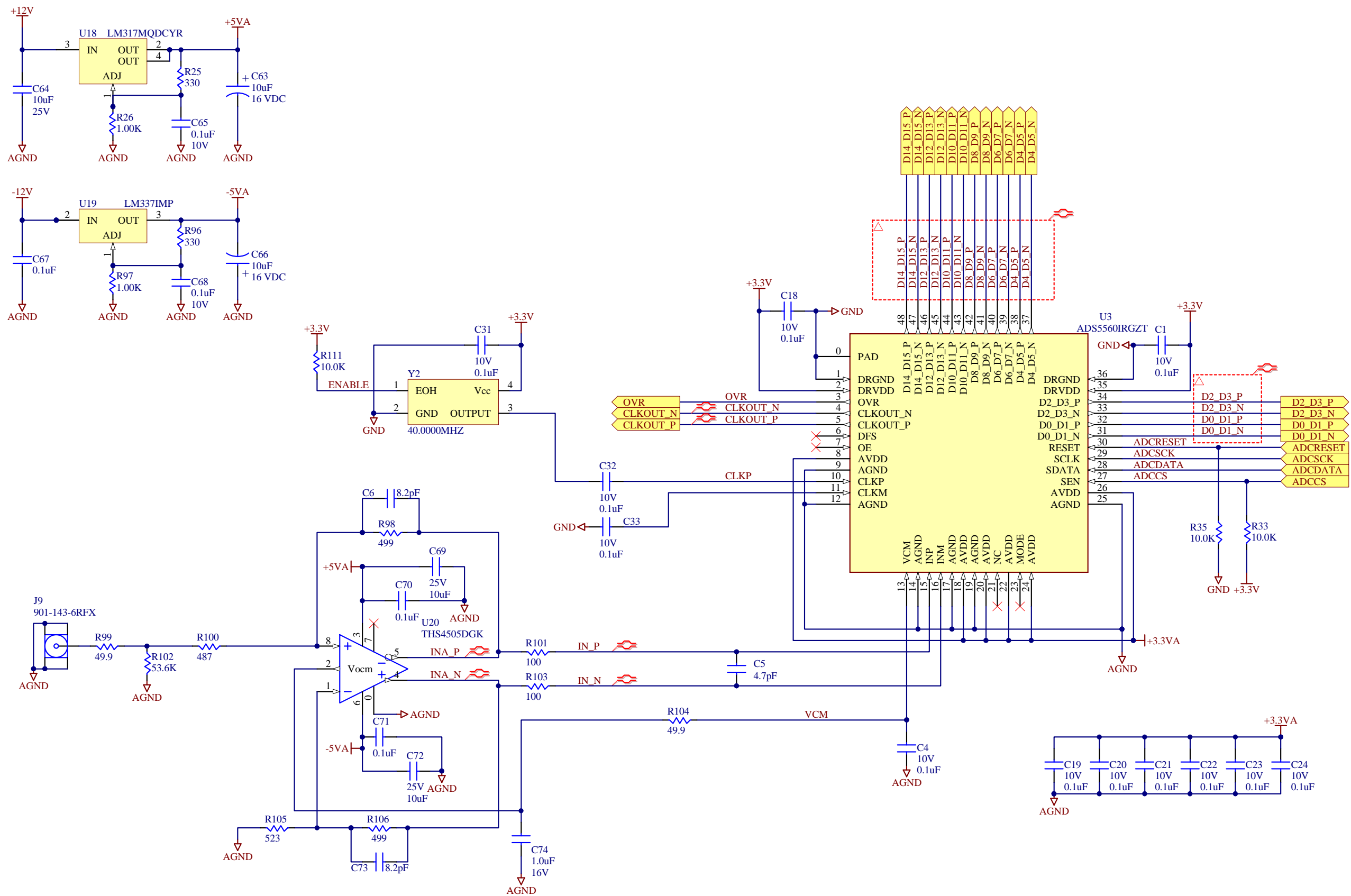
C

D

D







Title: **ADS5560**

Size: **B**

Date: 6/5/2013

SVN: 4623

Number: 80-000464R1-1_RevB

Time: 12:11:50 PM

File: ADS5560.SchDoc

Rev: **A**

Sheet 5 of 6

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